

/ Descriptions

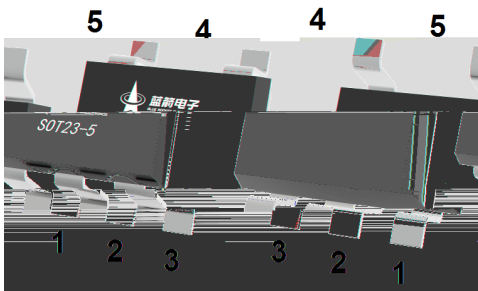
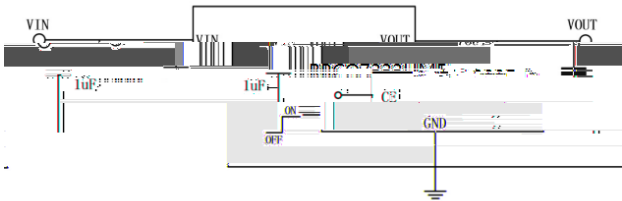
BRC07333HME	CMOS	s	H ₁	H ₂
30V	Q	CMOS	H ₁	

/ Features

- ◆ 30V
- ◆ χ 500mA ε $V_{IN}=V_{OUT}+2V$ Pulse testing
- ◆ H₁ 200mV@I_{out}=100mA
- ◆ H₁ 2.5 μ A
- ◆ $\pm$ 50ppm/
- ◆ $\pm$ 2
- ◆ PSRR χ 65dB@1kHz
- ◆ SOT23-5

/ Applications

- ◆
- ◆
- ◆



	a			a	
1	V _{IN}		4	NC	
2	GND		5	V _{OUT}	
3	CE				

/ Absolute Maximum Ratings($T_A=25^\circ\text{C}$)

			η
	V_{IN_MAX}	30	V
	V_{IN}	-0.3~30	V
CE	V_{CE}	$V_{IN}-0.3\sim V_{IN}+0.3$	V
(1)	I_{OUT}	0~500	mA
	P_D	0.3	W
	T_{stg}	-40 to +150	$^\circ\text{C}$
	T_{OP}	-40 to +85	

NOTE X (1) $V_{IN}=V_{OUT}+2V$
 V_{OUT}

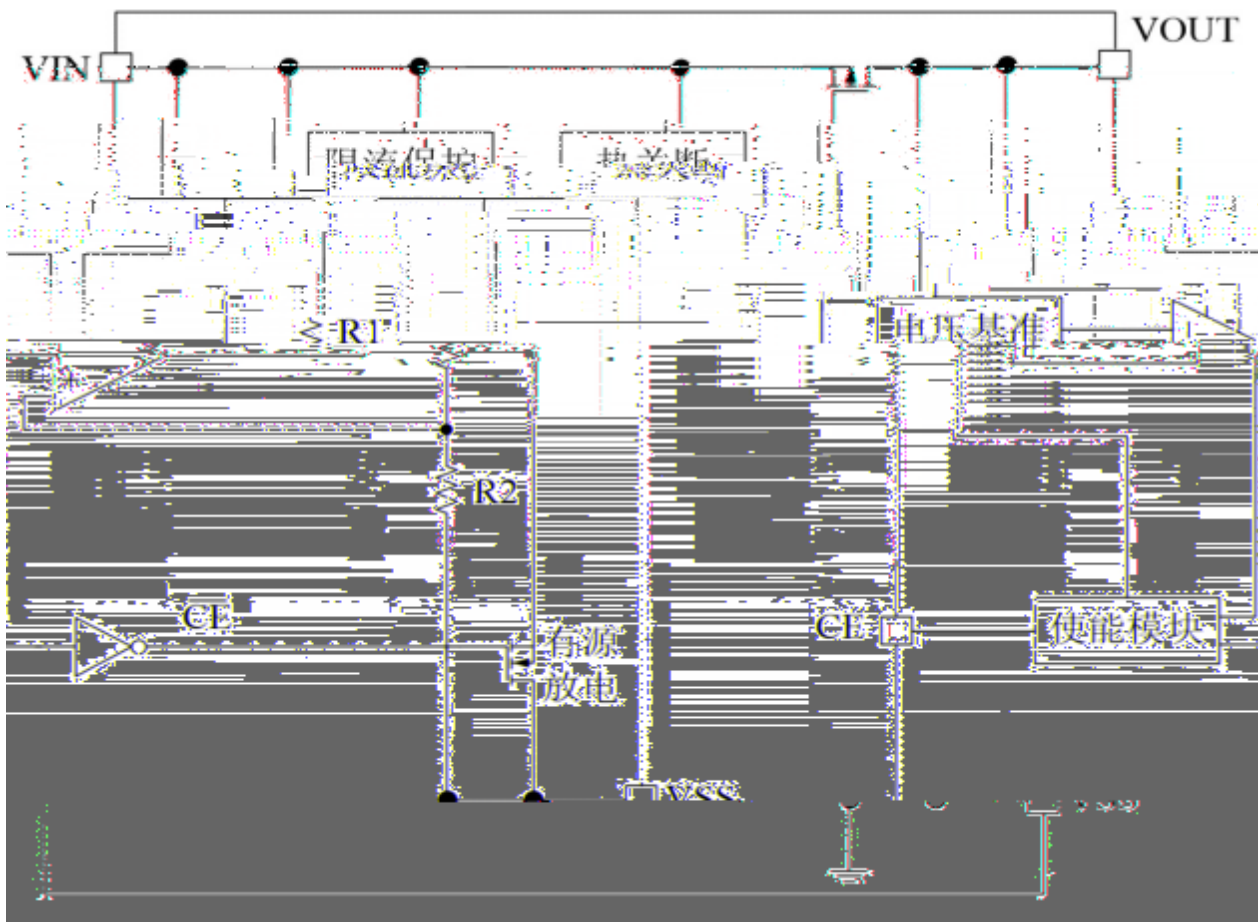
3s

a

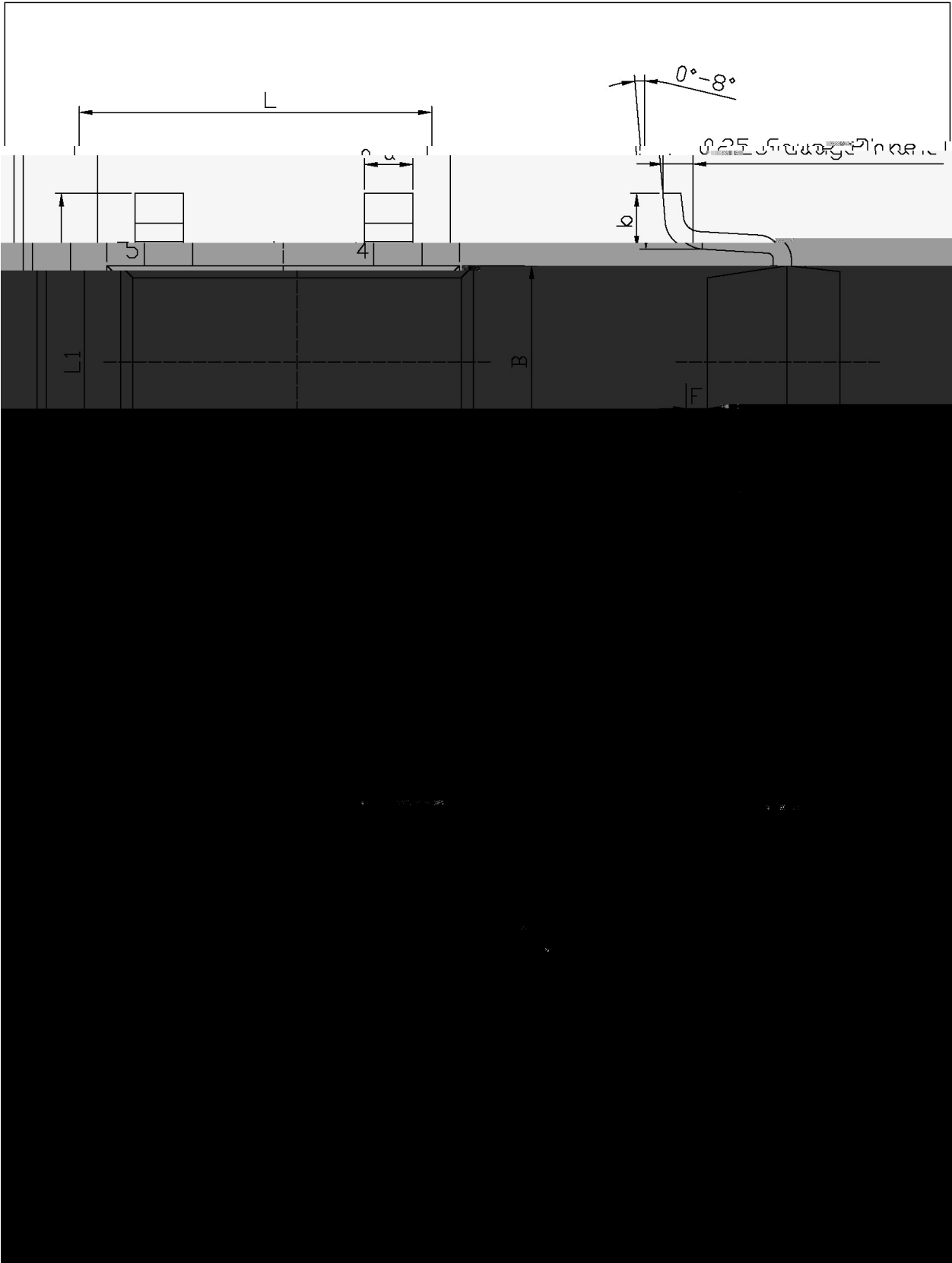
/ Electrical Characteristics($T_A=25^\circ\text{C}$, $V_{IN}=V_{OUT}+1V$, $C_{IN}=C_{OUT}=1\mu\text{F}$, unless otherwise specified)

						η
	V_{IN}		2.5		24	V
	V_{OUT}	$I_{OUT}=10\text{mA}$	$V_{OUT} \times 0.98$		$V_{OUT} \times 1.02$	V
	I_{SS}	$I_{OUT}=0\text{mA}$	-	2.5	6	μA
	I_{ST}	CE=0V			0.2	μA
	I_{LIM}	-	500			mA
	V_{DROP}	$I_{OUT}=100\text{mA}$	-	200	-	mV
	I_{SHORT}			55		mA
η	V_{CE_L}				0.4	V
	V_{CE_H}		1.5			V
	R_{DIS}	$V_{CE} = 0.4V$		350		Ω
	V_{LOAD}	$V_{IN}=V_{OUT}+1$ $1\text{mA} \leq I_{OUT} \leq 100\text{mA}$	-	5	20	mV
	V_{LINE}	$I_{OUT}=10\text{mA}$ $V_{OUT}+1V \leq V_{IN} \leq 20V$	-	0.05	-	%/V
	$V_{OUT}/$ ($T_A \times V_{OUT}$)	$I_{OUT}=1\text{mA}$ $-40 \leq T_A \leq +85$	-	± 50	-	ppm/
	PSRR	$V_{IN}=4.3V$	$f=100\text{Hz}$	-	80	-
		$V_{OUT}=3.3V$	$f=1\text{kHz}$	-	65	-
		$I_{OUT}=10\text{mA}$	$f=10\text{kHz}$	-	50	-
	T_{SD}	i	-	140	-	
	T_{SD}	ï	-	20	-	

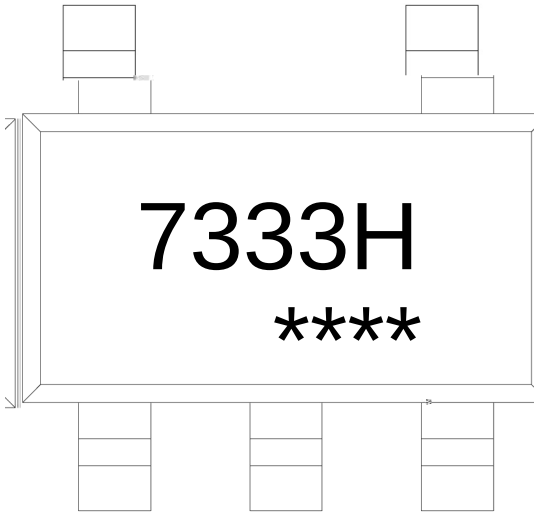
/ Functionl Block Diagram



/ Package Dimensions



/ Marking Instructions



X
7333H X
**** X

ψ

() / Temperature Profile for IR Reflow Soldering(Pb-Free)

1

X

150

Note: