

“Y / Descriptions

SOT-89 PNP Silicon PNP transistor in a SOT-89 Plastic Package.

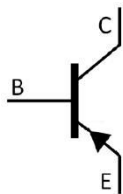
• È / Features

， ， 2SC2881G
High f_T , high V_{CEO} , small flat package, complementary pair with 2SC2881G.Halogen Free Product.

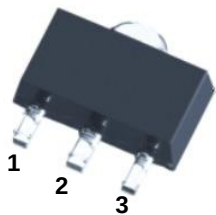
v / Applications

Power amplifier applications.

л ˆ ˆ ˆ / Equivalent Circuit



ˆ ˆ ˆ / Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

ˆ ˆ ˆ / Marking

h_{FE} Classifications Symbol	O	Y
h_{FE} Range	80 160	120 240
Marking	GDO * *	GDY * *

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-120	V
Collector to Emitter Voltage	V_{CEO}	-120	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current-Continuous	I_C	-800	mA
Collector Base-Continuous	I_B	-160	mA
Collector Power Dissipation	P_C	500	mW
Collector Power Dissipation*	$*P_C$	1.0	W
Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	

*mounted on ceramic substrate(250mm²×0.8t)

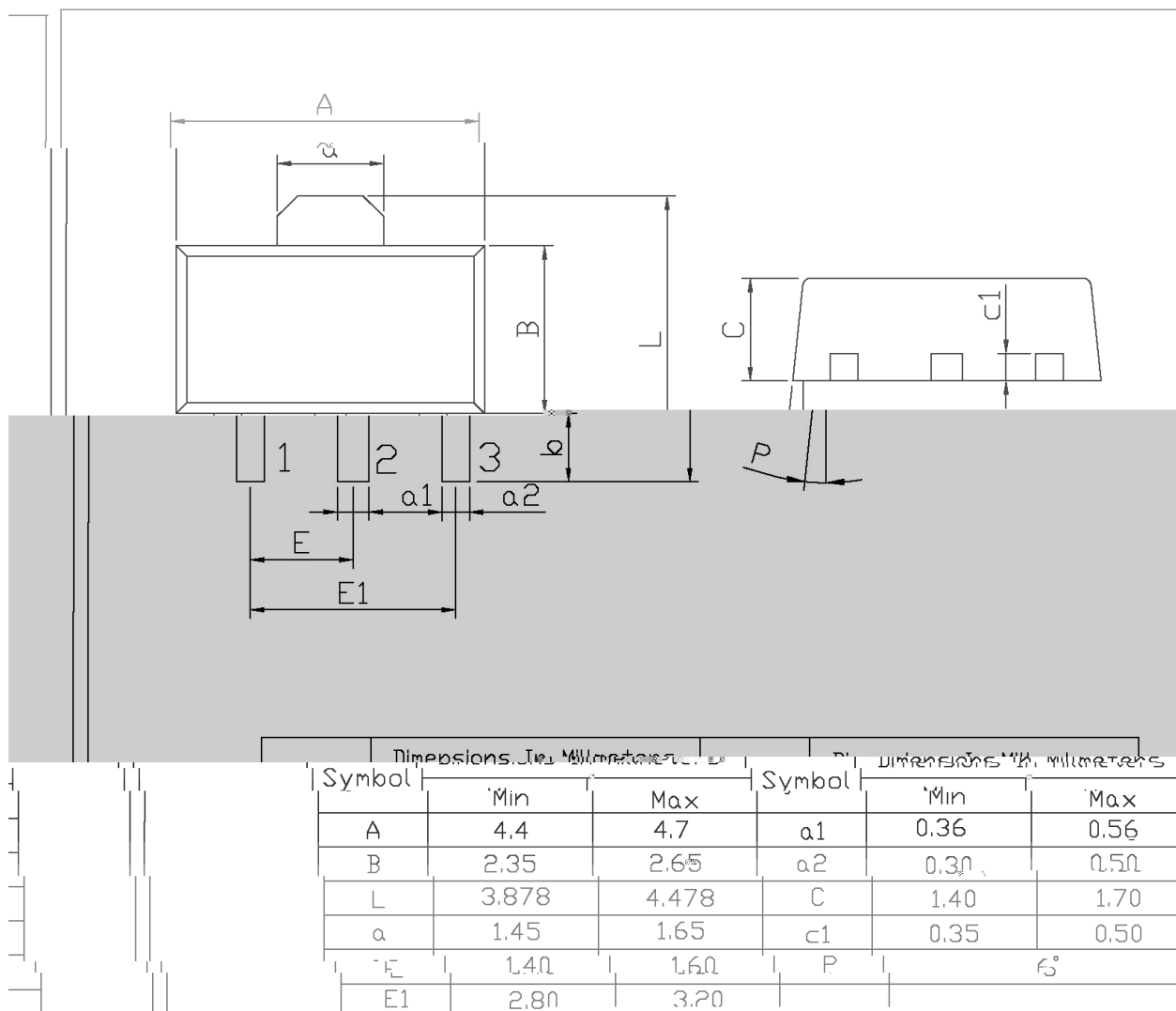
* 250mm²×0.8t

Parameter	Symbol	Test Conditions
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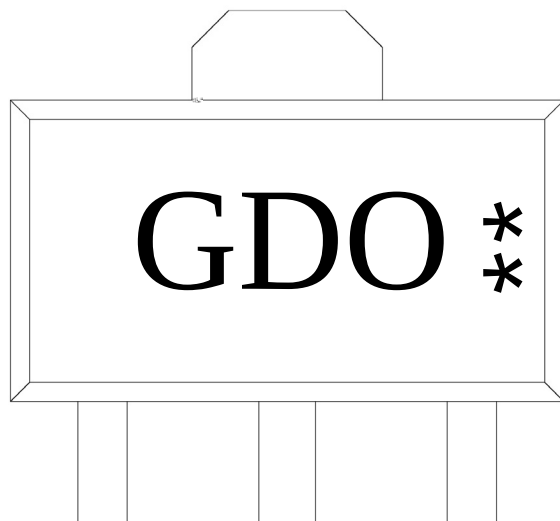
⊂ É Ò L f / Package Dimensions

⊂ É Ò L f

⊂ É Ò L f 单位: mm



1. 符号 / Marking Instructions



G

D

O

h_{FE}

**

Note:

G: Halogen Free Product.

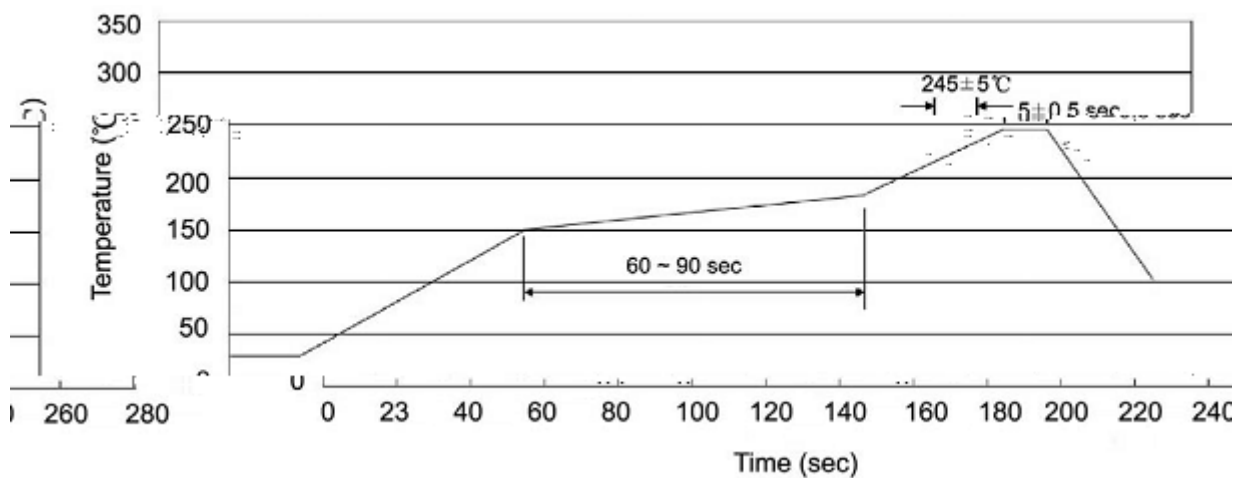
D: Product Type.

O: h_{FE} Classifications Symbol

** : Lot No. Code, code change with Lot No.



Whp shudwxuh Suriloh i ru LU Uhior z Vroghulqj+Se0lueh,



Note:

- | | | | |
|---|----------|-------------|---|
| 1 | 25 ~ 150 | 60 ~ 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 245±5 | 5±0.5sec; | 2.Peak Temp.:245±5 , Duration:5±0.5sec. |
| 3 | 2 ~ 10 | /sec. | 3. Cooling Speed: 2~10 /sec. |

Resistance to Soldering Heat Test Conditions

260±5

10±1 sec.

Temp.:260±5

Time:10±1 sec

Packaging SPEC.

/ REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm ³)		
	Units/Reel 只/卷盘	Reels/Inner Box 卷盘/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Reel	Inner Box 盒	Outer Box 箱
SOT-89	1,000	7	7,000	8	56,000	7 ×12	180×120×180	385×257×392

Notices